



CPUs

The high-performance CPU is based on the latest technology processor with fast computation and high throughput. The controller can manage up to 32K of I/O in a number of standard languages. The powerful CPU enables complex applications to be easily solved with the high performance processor and up to 64 Mbytes of user memory. The RX3i supports multiple IEC languages and C programming to give you program flexibility. The RX3i increases machine cycle times, reduces downtime with its extensive diagnostics and hot swap capability, and enables you to store large amounts of data to reduce external hardware cost.

	IC695CPE330	IC695CPK330	IC695CPE305
Product Name	RX3i CPU (only) with Ethernet port	RX3i CPU (with Energy Pack) with Ethernet port	RX3i CPU with built-in USB Master port, Ethernet port and serial port
Lifecycle Status	Active	Active	Active
Module Type	Controller	Controller	Controller
Backplane Support	Universal Backplane Only. Uses PCI Bus.	Universal Backplane Only. Uses PCI Bus.	Universal Backplane Only. Uses PCI Bus.
Boolean Execution Speed (ms/K)			.072
User Logic Memory	64Meg bytes	64Meg bytes	5Meg bytes
Battery Backed Real Time Clock	Yes	Yes	Yes
Dynamic Data Back-up	Battery Backup only	Energy Pack Support (Battery-less Backup)	Energy Pack Support (Battery-less Backup)
I/O Discrete Points	32K	32K	32K
I/O Analog Points	32K	32K	32K
Type of Memory Storage	1CFast (Very high speed Compactflash)	1CFast (Very high speed Compactflash)	SRAM, Flash
Processor Speed (MHz)	1.6GHz Dual Core	1.6GHz Dual Core	1.1GHz
USB -A 2.0 Master Port	Yes. CPU application upload/download to a Thumb Drive or Smart Phone	Yes. CPU application upload/download to a Thumb Drive or Smart Phone	Yes. CPU application upload/download to a Thumb Drive or Smart Phone
Built-in Ethernet Ports	One RJ-45 port, 10/100/1000Mbaud. One 2-port switch 10/100/1000	One RJ-45 port, 10/100/1000Mbaud. One 2-port switch 10/100/1000	One RJ-45 port, 10/100Mbaud. SRTP support for programmer only
Built-in Serial Ports	None. Serial functionality should be moved to the IC695CMM002 or IC695CMM004 when migrating to the CPE330.	None. Serial functionality should be moved to the IC695CMM002 or IC695CMM004 when migrating to the CPK330.	One RS-232 port. Supports SNP, Serial I/O, Modbus Slave and Modbus Master (Application code)
Total Number of Local Racks	8	8	8
Communications Options	IEC104, DNP3 outstation, IEC61850 client, HART SNP, SRTP, OPC-UA EGD	IEC104, DNP3 outstation, IEC61850 client, HART SNP, SRTP, OPC-UA EGD	Serial, Genius, CMX (Reflective Memory), Ethernet
Supported IO Protocols	PROFINET, EGD, Modbus TCP, PROFIBUS, Genius, DeviceNet, ModBus RTU, Reflective Memory (CMX)	PROFINET, EGD, Modbus TCP, PROFIBUS, Genius, DeviceNet, ModBus RTU, Reflective Memory (CMX)	PROFINET, EGD, Modbus TCP, PROFIBUS, Genius, DeviceNet, ModBus RTU, Reflective Memory (CMX)
Software Programming Support	Machine Edition Logic Developer PLC 8.60 SIM 8 or above	Machine Edition Logic Developer PLC 8.60 SIM8 or above	Machine Edition Logic Developer Professional edition 7.0 SIM 3 or above
Program Languages Supported	Ladder Logic, Structured Text, C, Function Block Diagram	Ladder Logic, Structured Text, C, Function Block Diagram	Ladder Logic, Structured Text, C, Function Block Diagram
Internal Power Used	+3.3 VDC: 0.0 A +5 VDC: 0.0A (up to 1.5 A if USB is fully loaded with 0.5 A) +24 VDC: 0.625A without Energy Pack, G280.750 A with IC695ACC402 Energy Pack	+3.3 VDC: 0.0 A +5 VDC: 0.0A (up to 1.5 A if USB is fully loaded with 0.5 A) +24 VDC: 0.625A without Energy Pack, G280.750 A with IC695ACC402 Energy Pack	+3.3 VDC: 1.0 A +5 VDC: 1.0 A (up to 1.5 A if USB is fully loaded with 0.5 A) +24 VDC: 0.5A at startup, 0.1 A during run time (Applies only if Energy Pack is connected to the CPE305.)
Number of Slots Module Occupies on Backplane	2	2	1
HART Pass-through	HART Pass-through – Fully integrated into the PLC system over a monitored communications network, you can simply and securely access HART instruments directly to remotely manage and mitigate operational issues with no additional equipment required.		